

## ABSTRACT OF THE DISCLOSURE

A device capable of correcting time stamp value according to an embodiment of the present invention including an original time counter configured to generate and output  
5 an original time value, a subtracter configured to receive a network time value from the outside and the original time value from the original time counter, subtract the original time value from the network time value, and output the difference as a cycle-time difference value, and a first adder configured to receive an original time-stamp value generated from the original time value and the cycle-time difference value from the  
10 subtracter, add the cycle-time difference value to the original time-stamp value, and output the sum as a time-stamp value.